

Notice of References Cited	Application/Control No. 09/943,078	Applicant(s)/Patent Under Reexamination ABBOTT, TODD R.
	Examiner Erik Kielin	Art Unit 2813

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	B	US-6,534,393 B1	03-2003	Zhou et al.	438/618
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	M	US-			

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	U	Ghandi, VLSI Fabrication Principles, 2nd ed., John Wiley & Sons: New York, 1994, pp. 452, 456.
	V	Van Zant, Microchip Fabrication, 4th ed. McGraw-Hill: New York, 2000, p. 199.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.